

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Chun Hung Ning, Laurent R. Moll, Kwong-Tak Chui, Shun Wai Go, Piyush Shashikant Jamkhandi

Assignee: Broadcom Corporation

Title: Programmable Inter-Virtual Channel and Intra-Virtual Channel Instructions Issuing Rules for an I/O Bus of a System-on-a-Chip Processor

Serial No.: 10/821,397 Filing Date: April 9, 2004

Examiner: Paul R. Myers Group Art Unit: 2112

Docket No.: BP 3384 Customer No.: 34399

Austin, Texas
February 12, 2007

COMMISSIONER FOR PATENTS
PO Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT WITH FEE

Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, Applicants wish to call the following documents to the attention of the Examiner.

A PTO form 1449 listing these documents is enclosed.

Citation of the above documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described above; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

The information disclosure statement fee is being paid via the USPTO EFS. The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account 502264.

FILED ELECTRONICALLY
February 12, 2007

Respectfully submitted,

/Gary W. Hamilton/

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U.S. Department of Commerce, Patent and Trademark Office		Attorney Docket No.	Serial No.
		BP 3384	10/821,397
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Applicant(s)	
(Use several sheets if necessary)		Chun Hung Ning et al.	
		Filing Date	Group
		April 9, 2004	2112

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	2004/0068616	04/2004	Tierney et al.	711	141	
	AB	2004/0066758	04/2004	Van Doren et al.	370	329	
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents

							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL						<input type="checkbox"/>	<input type="checkbox"/>
	AM						<input type="checkbox"/>	<input type="checkbox"/>
	AN						<input type="checkbox"/>	<input type="checkbox"/>
	AO							
	AP							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AR	D. Linder et al., "An Adaptive and Fault Tolerant Wormhole Routing Strategy for k -ary n -cubes," IEEE Transactions on Computers, Vol. 40, No. 1, January 1991, 11 pages
	AS	W. Dally et al., "Deadlock-Free Adaptive Routing in Multicomputer Networks Using Virtual Channels," IEEE Transactions on Parallel and Distributed Systems, Vol. 4, No. 4, April 1993, 10 pages
	AT	J. Duato, "A Theory of Deadlock-Free Adaptive Multicast Routing in Wormhole Networks," IEEE Transactions on Parallel and Distributed Systems, Vol. 6, No. 9, September 1995, 12 pages

Examiner	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.